

07/13/01
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Docket No. M-11882 US

July 13, 2001

Box Patent Application
Commissioner For Patents
Washington, D. C. 20231

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Berger, Alexander J.; Kretz, Frank E.

Title: Alignment Of Semiconductor Wafers And Other Articles

X Return Receipt Postcard
X This Transmittal Letter (in duplicate)
8 page(s) Specification (not including claims)
3 page(s) Claims
1 page Abstract
7 Sheet(s) of Drawings (Figs. 1, 2, 3, 4, 5, 6, 7, 8, 9, and 10)
2 page(s) Declaration For Patent Application and Power of Attorney (unexecuted)
1 Information Disclosure Statement Under 37 CFR § 1.97(b)
4 page(s) PTO Form 1449
49 References

CLAIMS AS FILED

For	Number <u>Filed</u>		Number <u>Extra</u>		<u>Rate</u>		Basic Fee
Total Claims	24	-20 =	4	x	\$ 18.00	=	\$ 72.00
Independent Claims	8	-3 =	5	x	\$80	=	\$ 400.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$
<input type="checkbox"/>	Fee for Request for Extension of Time						\$

Please make the following charges to Deposit Account 19-2386:

- ☒ Total fee for filing the patent application in the amount of \$ 1,182.00
☒ The Commissioner is hereby authorized to charge any additional fees which may be
required, or credit any overpayment to Deposit Account 19-2386.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

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